

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S6	8	(fanout fan-out)same model\$3 and (logical adj effort)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/09/28 19:07
S14	3	logical adj effort same (fanout fan-out)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/09/28 19:32
S16	21	logical adj2 effort and (fanout fan-out) and transistor	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/09/28 19:33
S17	16	logical adj2 effort and (fanout fan-out) and transistor and model\$3	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/09/28 19:37
S18	14	(logical adj effort) and (fanout fan-out) and transistor and model\$3 and siz\$3	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/09/28 19:44
S27	40	siz\$3 same model\$3 same fanout	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/09/28 19:51
S32	157	siz\$3 and model\$3 and fanout and (dynamic) and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/09/28 19:57
S31	12	siz\$3 same model\$3 same fanout and (dynamic) and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/09/28 19:57
S35	10	logical adj effort same model\$3	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/09/28 20:04
S37	17	logical adj effort and siz\$3 and model\$3 and (fan-out fanout)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/09/28 20:16
S40	13	(logical adj effort) and siz\$3 and transistor and model\$3 and (fan-out fanout)and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/09/28 20:18
S44	1	(logical adj effort) and siz\$3 and (transistor element gate cell) and model\$3 and (fan-out fanout)and (replac\$3 chang\$3) and steady and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/09/28 20:22

S48	12	(logical adj effort) and (siz\$3 near4 model\$3) and (fan-out fanout)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/09/29 14:26
S53	0	(logical adj effort) and (fan-out fanout) and (square adj root) and (current voltage)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/09/29 19:30
S55	0	(logical adj effort) and (fan-out fanout) and (square adj root) and (current voltage) and proportional and model\$3	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/09/29 19:31
S54	0	(logical adj effort) and (fan-out fanout) and (square adj root) and (current voltage) and proportional	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/09/29 19:31
S56	0	(logical adj effort) and (fan-out fanout) and (square adj root) and (current voltage) and proportional and model\$3 and resistor	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/09/29 19:32
S52	25	(logical adj effort) and (fan-out fanout)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/09/30 13:19
L2	17	(logical adj effort) and (fan-out fanout) and Sutherland	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/09/30 13:38
L9	19	(logical adj effort) and (fan-out fanout) and (gate transistor cell) near4 (width siz\$3)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/09/30 13:46
L6	15	(logical adj effort) and (fan-out fanout) and transistor near4 (width siz\$3)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/09/30 13:46